



**Manas Maity**

*Department of Physics, Visva-Bharati, Santiniketan 731235, India  
CMS Collaboration, CERN, CH-1211 Geneva 23, Switzerland*

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The Department of Physics, Visva-Bharati is participating in the CMS experiment at the Large Hadron Collider (LHC), CERN, Geneva with funding from the Department of Science & Technology, Government of India. More than 3000 scientists and PhD students from leading institutes of the world including a few from India are participating in it. We contribute to the analysis of large volume of data collected by CMS; We have a local computing cluster which is part of the LHC-Grid computing framework; operation & maintenance of the detector; R&D for the upgrade of the detector.

Applications are invited for the following positions at Visva-Bharati, Santiniketan.

1. **Junior Research Fellow/ Research Assistant (1)** - The candidate will take part in analysis of pp collision data collected by the CMS Experiment from the Run II of the LHC and contribute to operation, maintenance and upgrade of the detector.

**Essential Qualifications:**

- i) M. Sc. in Physics with at least 60% marks or equivalent grade;
- ii) CSIR-UGC NET (JRF/LS), GATE, JEST qualified; if suitable candidate is not available the position may be treated as Research Assistant.

**Desirable Qualifications:**

- i) Knowledge of Particle Physics, particle detectors;
- ii) Knowledge of C/C++ programming language.

2. **Junior Research Fellow/ Research Assistant (1)**

The candidate will take part in development of DC Power Supply for a large distribution network, related monitoring and control systems.

**Essential Qualifications:**

- i) M. Sc. in Physics with at least 60% marks or equivalent grade
- ii) CSIR-UGC NET (JRF/LS), GATE, JEST qualified; if suitable candidate is not available the position may be treated as Research Assistant.
- iii) Good knowledge of Electronics, devices.
- iv) Good knowledge of programming in microprocessor/microcontroller, and C/C++.

**Desirable Qualifications:**

- i) Knowledge of DC power supply – design, DC-DC conversion;
- ii) PCB designing.
- iii) Having done a project in electronics.

**Pay** – Rs. 25,000 + HRA for JRF, Rs. 10,000 consolidated for RA

**Age** : Not more than 25 years 30.08.2016

**Duration**: Till 31<sup>st</sup> March, 2019(likely to be extended)

Candidates are requested to appear in an walk-in interview on 30.08.2016 at 11-00 hours at Siksha-Bhavana, Visva-Bharati, Santiniketan and bring relevant documents – mark-sheets, certificates, proof of age, etc. Second class railway fare by the shortest route or equivalent will be paid.

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