

CORRIGENDUM to Notice inviting Tender No. Ph-1/2016-2017/FIST/CompCluster, dated 20.6.2016

**Department of Physics
Visva-Bharati**

The changes mentioned below should be taken into account in the invitation of tender notice given in the link: http://www.visvabharati.ac.in/files/888200616_tenderphy.pdf

Page 21 (Annexure-I): Servers: Master Node –Processor: 2 x Intel Xeon E5-2640 v3
2.6GHz, 20M Cache,

Should Read as

Processor: 2 x Intel® Xeon® Processor **E5-2640 v4** (25M Cache, 2.40 GHz, 10 Cores / 20 Threads, 8 GT/s Intel® QPI) or higher

Page 21 (Annexure-I): Servers: Master Node – Memory: 4x 16GB RDIMM, 2133 MT/s, Dual Rank, x4 Data Width

Should read as

Memory: 2x 32 GB **DDR4 2400** MHz dual-rank registered ECC memory

Page 21 (Annexure-I): Servers: Compute Node – Processor: 2 x Intel Xeon E5-2640 v3
2.6GHz, 20M Cache,

Should read as

Processor: 2 x Intel® Xeon® Processor **E5-2640 v4** (25M Cache, 2.40 GHz, 10 Cores / 20 Threads, 8 GT/s Intel® QPI) or higher

Page 21 (Annexure-I): Servers: Compute Node – Memory: 8x 16GB RDIMM, 2133 MT/s, Dual Rank, x4 Data Width

Should read as

Memory: 4 x 32 GB **DDR4 2400** MHz dual-rank registered ECC memory

Any further changes will be uploaded in the official website of the institute.

Sincerely,

f Swapan Mandal 30/6/2016
Head, Department of Physics
प्रोफेसर एवं विभागाध्यक्ष/Professor and Head
भौतिकी विज्ञान विभाग/Department of Physics
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